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(Depositor)	Pan Tompkins ,
(Sig	for Tomphus
	January 24, 2007

APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/769,591	01/30/2004	-	Chidamber R. Kulkarni		X-1557-3 US	8956
TITLE OF INVENTION I			ICATION-SPECIFIC PR	OGRAMMABLE	MEMORY ARCHITECT	URE AND
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE	FEE TOTAL FEE(S) DU	E DATE DUE
nonprovisional	NO	\$1400	\$0	\$0	\$1400	02/14/2007
EXAM	IINER	ART UNIT	CLASS-SUBCLASS			
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OMB 0651-0033

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PAGE 2/2 * RCVD AT 1/24/2007 2:24:14 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-2/1 * DNIS:2732885 * CSID:408 377 6137 * DURATION (mm-ss):01-08

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O. Box 1450 Alexandria, Virginia 22313-1450

Inventor(s):

Chidamber R. Kulkarni et al.

Assignee: XILINX, INC. Serial No.: 10/769,591

conf.no. 8956

Filed:

January 30, 2004

Title: Method and Apparatus for Application-Specific Programmable Memory Architecture

and Interconnection Network on a Chip

Docket No.:

X-1557-3 US

Enclosed: Return Receipt Postcard

Fee Transmittal Form - Part B

Date: January 24, 2007

VIA FACSIMILE 571-273-2885

Atty/Sec: MRH/pat